

HERITAGE INSTITUTE OF TECHNOLOGY

TIME TABLE
<ECE (VLSI)>

M. Tech 2nd Year 1st Semester

SESSION: 2025-2026

DAY		9.00 AM – 10.00 AM	10.00 AM – 11.00 AM	11.00 AM – 12 NOON	12 NOON – 1.00 PM	1.00 PM – 2.00 PM	2.00 PM – 3.00 PM	3.00 PM – 4.00 PM	4.00 PM – 5.00 PM	5.00 PM – 6.00 PM
MON	Gr.1	MENTORIN G/ICT615	L/OE/MATH 6121/MS/ICT 306	PROJECT/VLSI6195/ICT60		BREAK	L/PE5/VLSI6 132/KM/ICT 615			
	Gr. 2									
TUE	Gr.1	LIBRARY	L/OE/MATH 6121/DC/ICT 306	L/OE/MATH 6121/MS/ICT 306	BREAK	L/PE5/VLSI6 132/KM/ICT 615	PROJECT/VLSI6195/ICT602			
	Gr. 2									
WED	Gr.1	PROJECT/VLSI6195/ICT602			BREAK	PROJECT/VLSI6195/ICT602				
	Gr. 2									
THU	Gr.1	PROJECT/VLSI6195/ICT602			BREAK	PROJECT/VLSI6195/ICT602				
	Gr. 2									
FRI	Gr.1	LIBRARY	L/PE5/VLSI6 132/KM/ICT 615	MENTORIN G/ICT615	BREAK	PROJECT/VLSI6195/ICT602				
	Gr. 2									

To be Effective from 09.07.2025

A minimum of 75% attendance is mandatory for being eligible to sit for the End-Semester Examination.
Students should target 100% attendance

Kasturi Mukherjee
Member


HOD


Principal

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Faculty Details:

INITIALS	FULLNAME	DEPT			
KM	Prof. (Dr.) Kasturi Mukherjee	ECE			
MS	Prof. Moulipriya Sarkar	MATH			
DC	Prof. (Dr.) Dipankar Chakraborty	MATH			

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COURSE STRUCTURE:

2nd Year, Semester I

A. Theory								
Sl. No.	Course Type	Code	Course Title	Contact Hours/Week				Credits
				L	T	P	Total	
1	Professional Elective PE-5	VLSI6131	Nano materials and Nano Technology	3	0	0	3	3
		VLSI6132	RF IC Design and MEMS					
2	Open Elective	MATH6121	Optimization Techniques	3	0	0	3	3
		CSEN6121	Business Analytics					
		ECEN 6125	Design and Technology for Photonic Integrated Circuit					
		AEIE6123	Intelligent Control					
Total of Theory				6	0	0	6	6

B. Sessional								
1	Dissertation	VLSI6195	Dissertation Phase I	0	0	20	20	10
Total of Semester				6	0	20	26	16

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